

AMENDMENT TRANSMITTAL LETTER

Docket No.
M4065.0014/P014 B

Application No.
09/940,791

Filing Date
August 29, 2001

Examiner
K. Rose

Art Unit
2822

Applicant(s): Larry D. Kinsman

Invention: LOW PROFILE BALL GRID ARRAY PACKAGE

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	6	- 20 =		x	0.00
Independent Claims	2	- 3 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00

☒ Large Entity

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☒ No additional fee is required for this amendment.

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Dated: July 3, 2002

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Docket No.: M4065.0014/P014-B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

7/13/02

In re Patent Application of:

Larry D. Kinsman ✓

Application No.: 09/940,791 ✓

Group Art Unit: 2822

Filed: August 29, 2001 ✓

Examiner: K. Rose

For: LOW PROFILE BALL GRID ARRAY
PACKAGE

AMENDMENT

Box Non-Fee Amendment
Commissioner for Patents
Washington, DC 20231

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Dear Sir:

This paper is responsive to the Office Action dated April 5, 2002, rejecting claims 39-44, Applicant amends the above-captioned application as follows:

IN THE CLAIMS:

Please replace claims 39 and 42 with amended claims 39 and 42.

39. (amended) A processor system comprising:

a central processing unit; and

a memory device connected to said central processing unit, said memory device comprised of a plurality of low profile ball grid array semiconductor packages, said low profile ball grid array semiconductor packages comprised of a base substrate having a top surface and a bottom surface, with an aperture therein which extends from said top surface to said bottom surface,